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QUERY CONTROL FORM		RTIS USE ONLY	
Application No. <u>09/838,520</u>	Prepared by <u>MPB</u>	Tracking Number <u>05891235</u>	
Examiner-GAU <u>DeBerardinis - 2836</u>	Date <u>3/22/04</u>	Week Date <u>1/19/04</u>	
	No. of queries <u>2</u>	<u>1FW</u>	

JACKET			
a. Serial No.	f. Foreign Priority	k. Print Claim(s)	<u>g. PTO-1449</u>
b. Applicant(s)	g. Disclaimer	l. Print Fig.	q. PTOL-85b
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract
d. PCT	i. Title	n. PTO-270/328	s. Sheets/Figs
e. Domestic Priority	j. Claims Allowed	o. PTO-892	t. Other

SPECIFICATION	MESSAGE
a. Page Missing	<p>① <u>PTO-1449: Please either initial or line through citations. (copy provided for reference).</u></p> <p>② <u>Claims 2, 5, 6, 7 and 11 (originally claims 16, 19, 20, 21 and 22, respectively), all depends on claim 14 (originally claim 15). — see attached.</u></p> <p><u>Please advise/correct claim dependency.</u></p> <p><i>[Signature]</i></p>
b. Text Continuity	
c. Holes through Data	
d. Other Missing Text	
e. Illegible Text	
f. Duplicate Text	
g. Brief Description	
h. Sequence Listing	
i. Appendix	
j. Amendments	
k. Other	
CLAIMS	
a. Claim(s) Missing	
<u>b. Improper Dependency</u>	
c. Duplicate Numbers	
d. Incorrect Numbering	
e. Index Disagrees	
f. Punctuation	
g. Amendments	
h. Bracketing	
i. Missing Text	
j. Duplicate Text	
k. Other	
	RESPONSE
	<p>① <u>Claim dependency corrected</u></p> <p>② <u>IDS Signed</u></p> <p><i>[Signature]</i></p>
	<p>initials <i>[Signature]</i></p>

FORM PTO-1449/A and B (Modified)		APPLICATION NO.: 09/838,520	ATTY. DOCKET NO.: A00312.70393
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		FILING DATE: April 19, 2001	CONFIRMATION NO.: 9453
		APPLICANT: Geoffrey T. Haigh et al	
Sheet	1	of	1
		GROUP ART UNIT: 2836	EXAMINER: R.L. Deberadinis

U.S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		

FOREIGN PATENT DOCUMENTS

Examiner's Initials	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/ Country	Number	Kind Code			
PP		DE	19718420A1		Siemens AG	11-12-98	Y
PP		DE	19922123A1		Siemens AG	11-23-00	Y
PP		DE	19922127A1		Siemens AG	11-23-00	Y
PP		DE	19922127C2		Siemens AG	05-29-02	Y
PP		DE	19922128C1		Siemens AG	01-25-01	Y
PP		DE	19922129C1		Siemens AG	09-28-00	Y

OTHER ART — NON PATENT LITERATURE DOCUMENTS


Examiner's Initials	Cite No.	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)

EXAMINER: <i>Robert R. Burk</i>	DATE CONSIDERED: 3/31/04
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#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. __, filed __, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).

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Issue Classification 	Application No.	Applicant(s)	
	09/838,520	HAIGH ET AL.	
	Examiner	Art Unit	
	Robert DeBeradinis	2836	

ISSUE CLASSIFICATION									
ORIGINAL				CROSS-REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
307	412	307	89						
INTERNATIONAL CLASSIFICATION		257	531						
402 F	271.42								
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	/								

<i>Robert DeBeradinis</i> 12/9/03 (Assistant Examiner) (Date)	<i>Robert DeBeradinis</i> 12/9/03 (Primary Examiner) (Date)	Total Claims Allowed: 14
<i>Bele</i> 12-10-03 (Legal Instruments Examiner) (Date)		O.G. Print Claim(s) / O.G. Print Fig. /

<input type="checkbox"/> Claims renumbered in the same order as presented by applicant						<input type="checkbox"/> CPA		<input type="checkbox"/> T.D.		<input type="checkbox"/> R.1.47	
Final	Original		Final	Original		Final	Original		Final	Original	
1	1		31	61		91	121		151	181	
2	2		32	62		92	122		152	182	
3	3		33	63		93	123		153	183	
4	4		34	64		94	124		154	184	
5	5		35	65		95	125		155	185	
6	6		36	66		96	126		156	186	
7	7		37	67		97	127		157	187	
8	8		38	68		98	128		158	188	
9	9		39	69		99	129		159	189	
10	10		40	70		100	130		160	190	
11	11		41	71		101	131		161	191	
12	12		42	72		102	132		162	192	
13	13		43	73		103	133		163	193	
14	14		44	74		104	134		164	194	
15	15		45	75		105	135		165	195	
16	16		46	76		106	136		166	196	
17	17		47	77		107	137		167	197	
18	18		48	78		108	138		168	198	
19	19		49	79		109	139		169	199	
20	20		50	80		110	140		170	200	
21	21		51	81		111	141		171	201	
22	22		52	82		112	142		172	202	
23	23		53	83		113	143		173	203	
24	24		54	84		114	144		174	204	
25	25		55	85		115	145		175	205	
26	26		56	86		116	146		176	206	
27	27		57	87		117	147		177	207	
28	28		58	88		118	148		178	208	
29	29		59	89		119	149		179	209	
30	30		60	90		120	150		180	210	

In the Claims

Applicant has submitted a complete new claim set showing marked up claims with insertions indicated by underlining.

Amend claims 14 and 15 as follows:

Claims 1-13 (cancelled)

1 ~~14.~~ (currently amended) An information signal isolator comprising:
a first substrate;
a first passive component formed on the first substrate;
an isolation layer formed over the first passive component;
a second passive component formed over the isolation layer;
the first and second passive components being coils;
an input for receiving an input signal; and
a driver circuit coupled between the input and one of said passive components.

2 ~~15.~~ (currently amended) An information isolator comprising:
a first substrate;
a first passive component formed on the first substrate;
an isolation layer formed over the first passive component;
a second passive component formed over the isolation layer;
the first and second passive components being capacitor plates;
an input for receiving an input signal; and
a driver circuit coupled between the input and one of said passive components.

3 ~~16.~~ (previously presented) The isolator of claim ~~14~~ or claim ~~15~~ wherein the first substrate is a semiconductor substrate.

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~~17.~~ (previously presented) The isolator of claim ~~16~~, wherein the driver circuit also is formed on the first semiconductor substrate.

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~~18.~~ (previously presented) The isolator of claim ~~16~~, further comprising a second substrate, wherein the driver circuit is formed on the second substrate.

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^{1 2}
~~19.~~ (previously presented) The isolator of claim ~~14~~ or claim ~~15~~ wherein the first passive component is formed on top of the first substrate.

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^{1 2}
~~20.~~ (previously presented) The isolator of claim ~~14~~ or claim ~~15~~ wherein the first passive component is formed into the first substrate.

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^{1 2}
~~21.~~ (previously presented) The isolator of claim ~~14~~ or claim ~~15~~, further comprising a third passive component on the substrate, a second isolation layer over the third passive component, and a fourth passive component formed over the second isolation layer, wherein the driver circuit provides signals to the first and third passive components.

¹²

^{1 2}
~~22.~~ (previously presented) The isolator of claim ~~14~~ or claim ~~15~~, further comprising a third passive component on the substrate, a second isolation layer over the third passive component, and a fourth passive component formed over the second isolation layer, wherein the driver circuit provides signals to the second and fourth passive components.

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~~23.~~ (previously presented) The isolator of claim ~~22~~ wherein the first and second isolation layers are a single layer.

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¹
~~24.~~ (previously presented) The isolator of claim ~~14~~ wherein the first and second passive components are referenced to separate, galvanically isolated grounds, and further including a Faraday shield between the first and second passive components,

with the Faraday shield referenced to the same ground as the second passive components.

9 3
25. (previously presented) The isolator of claim ~~21~~, wherein the third and fourth passive components are capacitor plates.

13 8
26. (previously presented) The isolator of claim ~~21~~, wherein the first, second, third, and fourth passive components are coils.

11 10
27. (previously presented) The isolator of claim ~~26~~ wherein the first and second passive components are referenced to separate, galvanically isolated grounds, and further including a Faraday shield between the first and second passive components, with the Faraday shield referenced to the same ground as the second passive components.